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H Eda, K Takaiwa, A Hayashi - US Patent 5,030,951, 1991 - Google Patents

... FORMAT CONVERTING SYSTEM FOR SYNCHRONOUS OPTICAL NETWORK

Inventors: Hitoshi Eda,

Shimodate; Kazumaro Takaiwa, Oyama; Akihiro Hayasbi, Tochigi, all of Japan [73 ...

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MD Spiller, AR Newton - Proceedings of the 1997 IEEE/ACM international conference on ..., 1997 - portal.acm.org

Page 1. EDA and the Network ... In this approach, all proxies in the EDA network would be relatively simple and architecturally identical. ...

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M Eda - US Patent 5,289,302, 1994 - Google Patents

... Eda [54] ACCESS METHOD FOR OPTICAL LOCAL AREA NETWORK SYSTEMS [75]

Inventor: Masahiro

Eda, Tokyo, Japan [73] Assignee: NEC Corporation, Tokyo, Japan [21] Appi. ...

[Cited by 13](#) - [Related Articles](#) - [Web Search](#)**[VPI and VCI assignment system in ATM system - all 2 versions »](#)**

K Tanaka, S Eda, K Oomuro, R Hyodo, O Sekihata, H ... - US Patent 5,339,318, 1994 - Google Patents

... Tanaka et al. [54] VPI AND VCI ASSIGNMENT SYSTEM IN ATM SYSTEM. [75] Inventors: Kenji

Tanaka; Susumu Eda; Katsumi Oomuro ... NETWORK MANAGEMENT UNIT MANAGEMENT TABLE ...

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M Eda, M Fujiwara - US Patent 5,365,344, 1994 - Google Patents

... 75] Inventors: Masahiro Eda; Masahiko Fujiwara ... media-access control is easily carried out inde- pendently in each node apparatus in an optical network system by ...

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R Hyodo, S Eda, K Tanaka, K Oomuro, O Sekihata, H ... - US Patent 5,287,349, 1994 - Google Patents

Page 1. United States Patent Hyodo et al. [54] ATM EXCHANGE SYSTEM [75] Inventors: Ryuji Hyodo; Susumu Eda; Kenji Tanaka; Katsumi Oomuro; Osamu Sekihata ...

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K Tanaka, S Eda, K Oomuro, R Hyodo, O Sekihata, H ... - US Patent 5,373,504, 1994 - Google Patents

... Tanaka et al. [54] APPARATUS AND A METHOD FOR SETTING A COMMUNICATION


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systems. It also supports the design of programs **distributed** over a **network** of computers. Another goal is Department of Teleinformatics The EDA Multiprocessing Model L-E Thorelli Department

.11 6.3 Merging **Network** Example .

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Department of Teleinformatics **Distributed** Programming Toolkit for NUT V. Vlassov, E.

rNUT is based on the Extended Dataflow Actor model, EDA, developed at KTH. 4 Contents 1 Introduction . composed of heterogeneous computers in a **network**. Communication and synchronization between the [it.kth.se/labs/se/Reports/rnut.ps.Z](http://it.kth.se/labs/se/Reports/rnut.ps.Z)

[Internet-based Workflows: A Paradigm for Dynamically.. - Hemang Lavana \(1997\) \(Correct\) \(2 citations\)](#)

run on the Internet, user interface, groups of **distributed** users that require collaboration and other peer-reviewed benchmarking experiments in EDA (Electronic Design Automation) on the Internet. space, time and technologies. In particular, **network**-integrated computing on the Internet is an area [www.cbl.ncsu.edu/publications/1997-GROUP-Lavana/1997-GROUP-Lavana.ps.gz](http://www.cbl.ncsu.edu/publications/1997-GROUP-Lavana/1997-GROUP-Lavana.ps.gz)

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[EDA and the Network - Mark Spiller \(1997\) \(Correct\) \(2 citations\)](#)

an increasingly important role in the evaluation, **distribution**, integration, and management of EDA systems.

EDA and the **Network** Mark D. Spiller and A. Richard

EDA and the **Network** Mark D. Spiller and A. Richard Newton Department

[www-cad.eecs.berkeley.edu/~mds/research/pub/iccad97.ps](http://www-cad.eecs.berkeley.edu/~mds/research/pub/iccad97.ps)

[Optimisation of Global Data Transfer and Storage.. - Catthoor, Balasa, ... \(1997\) \(Correct\) \(1 citation\)](#)

hardware (DSP core and controller)and a **distributed** memory organisation which is usually expensive to help designers with this problem. Commercial EDA tools, such as SPW/HDS (Alta/Cadence Design) of loops)and in sophisticated communication **network** protocols, which handle large sets of records [ftp.imec.be/pub/vsdm/reports/memory\\_optimisation/hlmm\\_sum.ps.gz](http://ftp.imec.be/pub/vsdm/reports/memory_optimisation/hlmm_sum.ps.gz)

[Enhancing the Data Openness of Frameworks by.. - Böttger, Engel.. \(1994\) \(Correct\)](#)

of an industrial project partner)Moreover their **distribution**, heterogeneity, and autonomy pose many 1 CAx is a place holder for CAD, CASE, CAM, CAE, EDA, etc. Application Application Application systems of the enterprise, e.g. with hierarchical, **network**, relational, and object-oriented DBS, or file [www.c-lab.de/~aatools/publications/papers/1994/edaf-94\\_.ps.gz](http://www.c-lab.de/~aatools/publications/papers/1994/edaf-94_.ps.gz)


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Page 1. [54] **INTERNET SERVER ACCESS CONTROL AND MONITORING SYSTEMS ... OTHER PUBLICATIONS**Bina et al.. Secure Access to Data over the **Internet**, 1994, pp. ...[Cited by 301](#) - [Related Articles](#) - [Web Search](#)**[EDA and the network - all 5 versions »](#)**

MD Spiller, AR Newton - Proceedings of the 1997 IEEE/ACM international conference on ..., 1997 - portal.acm.org

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M Santarini, R Goering, EE Times - EE Times, 2001 - eedesign.com

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WP Heideman - Fabless Forum, 2001

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